

參考文獻

- [1] Y. Y. Tzou.,“ SP-Based Fully Digital Control of a PWM DC-AC Converter for AC Voltage Regulation,” IEEE Power Electronics Specialists Conference, Vol. 1, pp. 138 – 144, 1995.
- [2] H. K Kukrer. and N. S. Bayindir,“ Control strategy for single -Phase UPS inverters,” IEE Proceedings-Electric Power Applications, Vol. 150, pp. 743 – 746, 2003.
- [3] Y. Y. Tzou. and S. L. Jung., “ Full control of a PWM DC-AC converter for AC voltage regulation,” Aerospace and Electronic Systems, IEEE Trans on Vol. 34, pp. 1218 – 1226, 1998.
- [4] J. W. Baek., T. G. Koo., Y. B. Byun., K. Y. Joe. and D. H. Kim., “ A study on a single-phase module UPS using a three-arms converter and inverter,” IEEE Trans. on Industrial Electronics Society, Vol. 2, pp. 1282 – 1287, 2002.
- [5] J. J. Shieh., C. T. Pan. and Z. J. Cuey., “ Modelling and design of a reversible three-phase switching mode rectifier,” IEE Proceedings-Electric Power Applications, Vol. 144, pp. 389 – 396, 1997.
- [6] R. Wu., S. B. Dewan. and G. R. Slemon., “ Analysis of an AC-to-DC voltage source converter using PWM with phase

- and amplitude control, ” IEEE Trans. on Industry Applications, Vol. 27, pp. 355 – 364, 1991.
- [7] C. Turpin., L. Deprez., F. Forest., F. Richardeau. and T. A. Meynard., “ A ZVS imbricated cell multilevel inverter with auxiliary resonant commutated poles,” IEEE Trans. on Power Electronics, Vol. 17, pp. 874 – 882, 2002.
- [8] K. Siri., J. Banda., A. F. Khan. and I. Batarseh., “ Boost derived DC-to-AC converter with zero voltage switching,” Conference Record on Southcon, pp. 53 – 59, 1995.
- [9] C. T. Pan., Y. S. Huang, and T. L. Jong, “ A constantly sampled current controller with switch status dependent inner bound,” Industrial Electronics, IEEE Trans. Vol. 50, pp. 528 – 535, 2003.
- [10] H. V. d. Broeck. and M. Miller., “ Harmonics in DC to AC converters of single phase uninterruptible power supplies,” International Telecommunications Energy Conference, pp. 653 – 658, 1995.
- [11] P. A. Dahono. and E. Taryana., “ A new control method for single-phase PWM inverters to realize zero steady-state error and fast response,” International Conference on Power Electronics and Drive Systems, Vol. 2, pp. 888 – 892, 2003.

- [12] H. J. Cha., S. S. Kim., M. G. Kang. and Y. H. Chung.,
“ Real-time digital control of PWM inverter with PI
compensator for uninterruptible power supply,” IEEE Trans.
on Industrial Electronics Society, pp. 1124 – 1128, 1990.
- [13] J. F. Chen. and C. L. Chu., “ Combination voltage-controlled
and current-controlled PWM inverters for UPS parallel
operation,” IEEE Trans on Power Electronics, Vol. 10, pp.
547 – 558, 1995.
- [14] S. G. Lee., D. W. Kang., Y. H. Lee. and D. S. Hyun., “ The
carrier-based PWM method for voltage balance of flying
capacitor multilevel inverter,” IEEE Power Electronics
Specialists Conference, pp. 126 – 131, 2001.
- [15] X. Yuan., H. Stemmler. and I. Barbi., “Self-balancing of the
clamping-capacitor-voltages in the multilevel
capacitor-clamping-inverter under sub-harmonic PWM
modulation,” IEEE Trans. on Power Electronics, Vol. 16, pp.
256 – 263, 2001.
- [16] P. A. Dahono., A. Purwadi. and Qamaruzzaman., “ An LC
filter design method for single-phase PWM inverters,”
International Conference on Power Electronics and Drive
Systems, pp. 571 – 576, 1995.
- [17] M. P. Kazmierkowski. and L Malesani.,” Current control

- techniques for three-phase voltage-source PWM converters,” **IEEE Trans. on Industrial Electronics**, Vol. 45, pp. 691 – 703, 1998.
- [18] S. K. Chung., H. B. Shin. and H. W. Lee.,” Precision control of single-phase PWM inverter using PLL compensation,” **IEEE-Electric Power Applications**, Vol. 152.
- [19] L. Emery., I. Vasserman., O. Makarov., B. Deriy., S. Sasaki. and R. Soliday.,” Feedforward correction of the pulsed circularly polarizing undulator at the Advanced Photon Source,” **Particle Accelerator Conference**, Vol. 4, 2003.
- [20] B. P. McGrath., D. G. Holmes. and T. Lipo.,” Optimized space vector switching sequences for multilevel inverters,” **IEEE Trans Power Electronics**, Vol. 18.
- [21] D. Rathnakumar., J. L. Perumal. and T. Srinivasan.,” A new software implementation of space vector PWM,” **IEEE SoutheastCon**, pp. 131 – 136, 2005.
- [22] A. Tuladhar., H. Jin., T. Unger. and K. Mauch.,“ Parallel Operation of Single Phase Inverter Modules With No Control Interconnections,” **IEEE Power Electronics Conference and Exposition**, Vol. 1, pp. 94 – 100, 1997.
- [23] M. Lopez., J. L. Garcia de Vicuna., M. Castilla., J. Matas. and O. Lopez.,“ Control Design For Parallel-Connected DC-AC Inverters Using Sliding Mode Control,” **Power Electronics and**

Variable Speed Drives, Eighth International Conference, pp. 457 – 460, 2000.

- [24] H. V. D. Broeck. and U. Boeke,“ A Simple Method for Parallel Operation of Inverters,” International Telecommunications Energy Conference, pp. 143 – 150, 1998.**
- [25] T. Kawabata., N. Sashida., Y. Yamamoto., K. Ogasawara. and Y. Yamasaki,“ Parallel Processing Inverter System,” IEEE Trans. on Power Electronics, Vol. 6, pp. 442 – 450,1991.**
- [26] L. Xinchun., F. Feng., D. Shanxu., K. Yong. and C. Jian,“ The droop characteristic decoupling control of parallel connected UPS with no control interconnection,” IEEE International. on Electric Machines and Drives Conference, Vol. 3, pp. 1777 – 1780, 2003.**
- [27] K. Matsui., Y. Mura.i, M. Watanabe., M. Kaneko. and F. Ueda., “ A pulsewidth modulated inverter with parallel connected transistors using current-sharing reactors,” IEEE Trans. on Power Electronics, Vol. 8, pp. 186 – 191,1993.**
- [28] K. Matsui., Y. Mura.i, M. Watanabe., M. Kaneko. and F. Ueda., “ A pulsewidth modulated inverter with parallel connected transistors using current-sharing reactors,” IEEE Trans. on Power Electronics, Vol. 8, pp. 186 – 191,1993.**
- [29] J. M. Guerrero., L. G. D. Vicuna., J. Matas., M. Castilla. and**

- J. Miret., “ A wireless controller to enhance dynamic performance of parallel inverters in distributed generation systems,” IEEE Trans. on Power Electronics, Vol. 19, pp. 1205 – 1213, 2004.**
- [30] T. Kawabata. and S. Higashino., “ Parallel operation of voltage source inverters,” IEEE Trans. on Industry Applications, Vol. 24, pp. 281 – 287, 1988.**
- [31] L. Chen., L. Xiao., C. Gong. and Y. Yan., “ Circulating current's characteristics analysis and the control strategy of parallel system based on double close-loop controlled VSI,” IEEE Power Electronics Specialists Conference, Vol. 6, pp. 4791 – 4797, 2004.**
- [32] C. S. Lee., S. Kim., C. B. Kim., S. C. Hong., J. S. Yoo., S. W. Kim., C. H. Kim., S. H. Woo. and S.Y. Sun., “ Parallel UPS with a instantaneous current sharing control,” IEEE Ind. Applic., Proceedings of the 24th Annual Conference of the Vol. 1, pp. 568 – 573, 1998.**
- [33] Y. Ito. and O. Iyama., “ Parallel redundant operation of UPS with robust current minor loop,” Power Conversion Conference, Vol. 1, pp. 489 – 494, 1997.**
- [34] T. A. Meynard., H. Foch., P. Thomas., J. Courault., R. Jakob. and M. Nahrstaedt., “ Multicell converters: basic concepts**

- and industry applications,” **IEEE Trans. on Industrial**, Vol. 49, pp. 955 – 964, 2002.
- [35] J. F. Chen., C. L. Chu. and C. L. Huang., “ The parallel operation of two UPS by the coupled-inductor method,” **Industrial Electronics**, 1992., **Proceedings of the IEEE International Symposium on**, pp. 733 – 736, 1992.
- [36] F. Zanzuan. and Q. Wonlong., “ A current share method for parallel single-phase DC/AC inverter without control interconnection,” **Power Electronics and Motion Control Conference**, Vol. 2, pp. 953 – 955, 2004.
- [37] Y. K. Chen., T. F. Wu., Y. E. Wu. and C. P. Ku., “ A current-sharing control strategy for paralleled multi-inverter systems using microprocessor-based robust control, ” **Electrical and Electronic Technology**, 2001. **IEEE TENCON.**, **Proceedings of IEEE Region 10 International Conference on** Vol 2, pp. 647 – 653, 2001.
- [38] Y. Pei., G. Jiang., X. Yang. and Z. Wang., “ Auto-master-slave control technique of parallel inverters in distributed AC power systems and UPS,” **Power Electronics Specialists Conference**, Vol 3, 2004.
- [39] R. Wu., S. B. Dewan. and G. R. Slemon.,” **Analysis of an AC-to-DC voltage source converter using PWM with phase and amplitude control**,” **Industry Applications**, **IEEE Trans.**

on Power Electronics, Vol 27.

- [40] J. J. Shieh., C. T. Pan. and Z. J. Cuey.,” Modelling and design of a reversible three-phase switching mode rectifier,” Electric Power Applications, Vol 144.**
- [41] S. J. Chiang., C. H. Lin. and C. Y. Yen.,“ Current limitation control for multi-module parallel operation of UPS inverters,” Electric Power Applications, Vol. 151.**
- [42] S. W. Park., Y. Chung., J. H. Choi., S. Moon. And J. E. Kim., “Control schemes of the inverter-interfaced multi-functional dispersed generation”, Power Engineering Society General Meeting, Vol. 3, pp. 13-17, 2003.**
- [43] 江弦樟・譯,「電力電子學」,全華科技圖書,88年11月。**
- [44] X. Yuan., H. Stemmler. and I. Barbi., “ Investigation on the clamping voltage self-balancing of the three-level capacitor clamping inverter,” Power Electronics Specialists Conference, Vol. 2, pp. 1059 – 1064, 1999**