Bibliography

[1] Rajarshi Mukherjee and Seda Ogrenci Memik, "Systematic Temperature Sensor Allocation and placement for Microprocessors," *Proceedings of the 43rd conference on Design Automation,* pp. 542-547, 2006.

[2] Hector Sanchez *et al.*, "Thermal Management System for High Performance PowerPC^{TM} Microprocessors," *Proceedings of Compcon,* pp. 325-330, 1997.

[3] Kyeong-Jae Lee, Kevin Skadron and Wei Huang, "Analytical Model for Sensor Placement on Microprocessors," *International Conference Computer Design,* pp. 24-27, 2005.

[4] W. Huang, S. Ghosh, K. Sankaranarayanan, K. Skadron and M. R. Stan, "HotSpot: Thermal Modeling for CMOS VLSI Systems," *IEEE Transactions on Component Packaging and Manufacturing Technology,* 2005.

[5] David Brooks, Vivek Tiwari and Margaret Martonosi, "Wattch: A Framework for Architectural-level Power Analysis and Optimizations," *ISCA,* 2000.

[6] "SPEC-CPU2000, Standard Performance Evaluation Council, Performance Evaluation in the New Millennium, Version 1.1," 2000, http://www.specbench.org/osg/cpu2000.

[7] Michiel A. P. Pertijs, Kofi A. A. Makinwa, Johan H. Huijsing, "A CMOS Smart Temperature Sensor with a 3σ Inaccuracy of ± 0.1°C from -55°C to 125°C," *IEEE Journal of Solid-State Circuits, Vol. 40, NO. 12,* pp. 2805-1815, Dec. 2005.

[8] Souvik Mahapatra, P. Bharath Kumar, M. A. Alam, "Investigation and Modeling of Interface and Bulk Trap Generation during Negative Bias Temperature Instability of

P-MOSFETs," *IEEE Transactions on Electron Devices, Vol. 51, NO. 9,* pp. 1371-1379, Sep. 2004.

[9] M. A. Alam, "A Critical Examination of The Mechanics of Dynamic NBTI for PMOSFETs," *IEDM,* pp. 345-348, 2003.

[10] Heyer LJ, Kruglyak S, Yooseph S, "Exploring Expression Data: Identification and Analysis of Coexpressed Genes," *Genome Res.,* pp. 1106-1115, 1999 Nov;9(11).

[11] D. C. Burger and T. M. Austin, "The SimpleScalar Tool Set, Version 2.0," *Computer Architecture News,* pp. 13-25, 1997. 25(3).

[12] Rajarshi Mukherjee and Seda Ogrenci Memik, "Physical Aware Frequency Selection for Dynamic Thermal Management in Multi-core Systems" *International Conference on Computer Aided Design ,* pp. 547-552, 2006.

[13] Amit Kumar, Li Shang, Li-Shiuan Peh and Niraj K. Jha, "HybDTM: a Coordinated Hardware-software Approach for Dynamic Thermal Management" *Proceedings of the 43rd annual conference on Design automation,* pp. 548-553, 2006.

[14] Lin Yuan, Sean Leventhal and Gang Qu, "Temperature-Aware Leakage Minimization Technique for Real-Time Systems" *International Conference on Computer Aided Design,* pp. 761-764, 2006.